

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Patent Application of



Yukio OKUDA

Attn: Applications Branch

Application No. 09/964,540

Filed: September 28, 2001

For: METHOD AND APPARATUS
OF DETERMINING DEFECT-FREE
SEMICONDUCTOR INTEGRATED
CIRCUIT

RESPONSE TO NOTICE AND
SUBMISSION OF EXECUTED DECLARATION

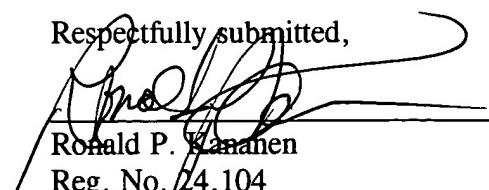
Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Notice to File Missing Parts of Application mailed October 31, 2001 (copy of which is returned herewith), attached hereto is a Declaration and Power of Attorney, duly executed by the inventor, referring to the above-identified serial number and filing date in compliance with 37 CFR §1.116. Please charge Deposit Account No. 18-0013 in the amount of \$130.00 in payment of the surcharge for other than a small entity.

The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No. 18-0013. A duplicate copy of this letter is enclosed for that purpose.

Respectfully submitted,


Ronald P. Kananen
Reg. No. 24,104

Dated: December 12, 2001

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SON-2230

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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P A T E N T & T R A D E M A R K O F F I C E
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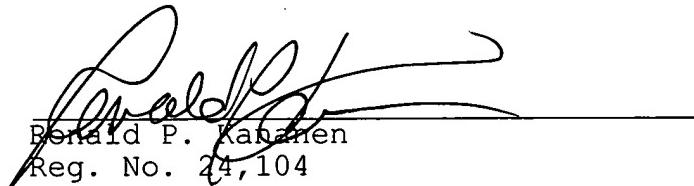
LETTER RE: SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents
Washington, D.C. 20231

Sir:

We have enclosed formal drawings in the above-identified application to replace the informal drawings submitted with the application on September 28, 2001.

Respectfully submitted,



Ronald P. Kananen
Reg. No. 24,104

Dated: December 12, 2001

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